

**Notice of References Cited**

Application/Control No.

10/729,458

Applicant(s)/Patent Under  
Reexamination  
CHIU ET AL.

Examiner

Mahmoud Dahimene

Art Unit

1765

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**NON-PATENT DOCUMENTS**

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	V	S. Wolf and R.N. Tauber, Silicon Processing for the VLSI Era, 1990, Lattice Press, Volume 2, Pages 318, 398
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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